

**Vendor** GENELEC  
**Model** 8430A

**Date** 2020-04-09  
**Version** A

## AES67-2018 PICS Summary

Transport	<input checked="" type="checkbox"/> Multicast <input type="checkbox"/> Unicast
QoS configuration	<input type="checkbox"/> Configurable markings with standard defaults <input type="checkbox"/> Fixed standard markings <input type="checkbox"/> Configurable, other defaults <input type="checkbox"/> Other markings, not configurable
Sample rates	<input checked="" type="checkbox"/> 48 kHz <input checked="" type="checkbox"/> 44.1 kHz <input checked="" type="checkbox"/> 96 kHz
Packet times	<input checked="" type="checkbox"/> 1 ms <input type="checkbox"/> 125 $\mu$ s <input type="checkbox"/> 250 $\mu$ s <input checked="" type="checkbox"/> 333 $\mu$ s <input checked="" type="checkbox"/> 4 ms
PTP profiles	<input type="checkbox"/> IEEE1588 Default <input checked="" type="checkbox"/> AES67 PTP Media <input type="checkbox"/> IEEE 802.1AS (AVB)
SDP transport	<input checked="" type="checkbox"/> Manually <input checked="" type="checkbox"/> SAP <input type="checkbox"/> SIP <input checked="" type="checkbox"/> Other (specify)
<i>Description</i>	RTSP/SDP
Discovery	<input type="checkbox"/> SAP <input checked="" type="checkbox"/> mDNS <input type="checkbox"/> NMOS IS-04 <input checked="" type="checkbox"/> Other (specify)
<i>Description</i>	Bonjour
Packet spacing (Transmitter)	<input checked="" type="checkbox"/> strict <input type="checkbox"/> loose
Jitter buffer (Receiver)	<input type="checkbox"/> small <input checked="" type="checkbox"/> large
SIP support	<input type="checkbox"/>

## Other interoperability disclosures

SMPTE ST 2110-30 support	<input checked="" type="checkbox"/>
Other PTP profiles	<input type="checkbox"/> SMPTE ST 2059-2 <input type="checkbox"/> Other (specify)
<i>Description</i>	